TI processors

Courses on TI SoCs based on ARM cores

ACSYS offers a large set of courses on TI processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming, which is needed to understand the boot program.
The course also contains an introduction to Code Composer Studio IDE. **TI3 - Cortex M4 Texas Instruments Implementation and Ti-RTOS** This course describes the Texas Instruments ARM Cortex M4F implementation and TI-RTOS real-time programming

TK1 - KEYSTONE II IMPLEMENTATION This course covers all SoCs belonging to the KeyStone II, AM5K2E, 66AK2H and 66AK2E