TI processors

Courses on TI SoCs based on ARM cores

ACSYS offers a large set of courses on TI processors.
Each course details both hardware and software implementation of these processors.
Examples are provided to explain low level programming, which is needed to understand the boot program.
The course also contains an introduction to Code Composer Studio IDE.

Vous pouvez visualiser les descriptifs détaillés des différents cours en utilisant la barre de navigation ci-dessus. Vous pouvez également cliquer sur les références des cours dans les descriptions ci dessous.

TI1 - TI STELLARIS CORTEX-M3 BASED MCUs IMPLEMENTATION This course covers all MCUs belonging to the Stellaris Cortex-M3 family, X00, 1000, 2000, 3000, 5000, 8000 & 9000 SERIES
TI2 - TI STELLARIS CORTEX-M4F BASED MCUs IMPLEMENTATION This course covers all MCUs belonging to the Stellaris Cortex-M4F family, LM4F100 and LM4F200 SERIES.

TK1 - KEYSTONE II IMPLEMENTATION This course covers all SoCs belonging to the KeyStone II, AM5K2E, 66AK2H and 66AK2E