



## Courses on ST processors based on ARM cores

ACSYS offers a large set of courses on ST processors.

Each course details both hardware and software implementation of these processors.

Examples are provided to explain low level programming and particularly how to use the software package provided by

ST. **Inquiry** **STG** **STM32 + FreeRTOS + LwIP** 5 days **Inquiry** **STR4** **STM32 F0 Series implementation** 4 days  
**STR5** **STM32 F1 Series implementation** 5 days **Inquiry** **STR6** **STM32 F2 Series implementation** 4 days  
**STR7** **STM32 F3 Series implementation** 5 days **Inquiry** **STR8** **STM32 F4 Series implementation** 4 days  
**STR9** **STM32 F4 Series implementation** 5 days **Inquiry** **RT3** **FreeRTOS Real-time**  
**embedded applications**. This is a high-weight, real-time operating system course designed to efficiently manage tasks in  
memory management. Using a real-time OS governing essential topics such as task scheduling, synchronization, and  
systems. Ideal for developers with development, enabling participants to design, implement, and debug robust embedded  
applications. 3 days **Inquiry** **STR1** **LwIP implementation** 2 days **Inquiry**