



Courses on NXP i.MX SoCs

ACSYS offers a large set of courses on NXP processors.

Each course details both hardware and software implementation of these processors.

Examples are provided to explain low level programming, which is needed to understand the boot program.

For on-site trainings, an additional day covering Linux porting or Windows Embedded porting may be appended to i.MX processor courses.

Reference Manual, being still only available under a Non-disclosure Agreement, contact information@ac6formation.com for any further details.

FA4 i.MX6 Implementation - 5 days **Inquiry**

FA5 i.MX8m Implementation - 5 days **Inquiry**

FA6 i.MX8Max Implementation - 5 days **Inquiry**

FC1 i.MX7M2 Implementation - 4 days **Inquiry**

NP1 i.MX2XX microcontroller implementation - 4 days **Inquiry**

NP2 i.MX RT1050 Real-time OS implementation - 5 days **Inquiry**

NP3 i.MX RT1050 Real-time OS implementation - 5 days **Inquiry**

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